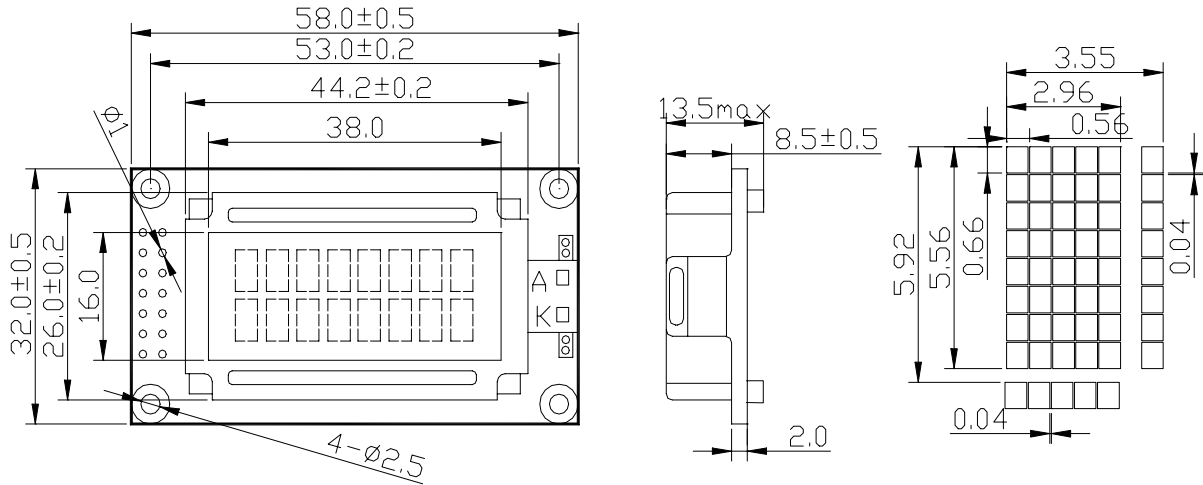


JXD0802B

*8 Characters 2 lines *Controller LSI built-in *+5V single power supply

EXTERNAL DIMENSIONS AND DISPLAY PATTERN



PIN CONNECTION

MECHANICAL DATA (Nominal dimensions)

module size----- 58.0Wx32.0Hx10.5D mm
 Effective display area----- 38.0Wx16.0H mm
 Dot size ----- 0.56Wx0.66H mm
 Character size ----- 2.96x5.56 mm

ABSOLUTE MAXIMUM RATINGS min max

Power supply for logic(Vdd-Vss) ----- -0.3 5.5V
 Input voltage(Vi) ----- -0.3VddV

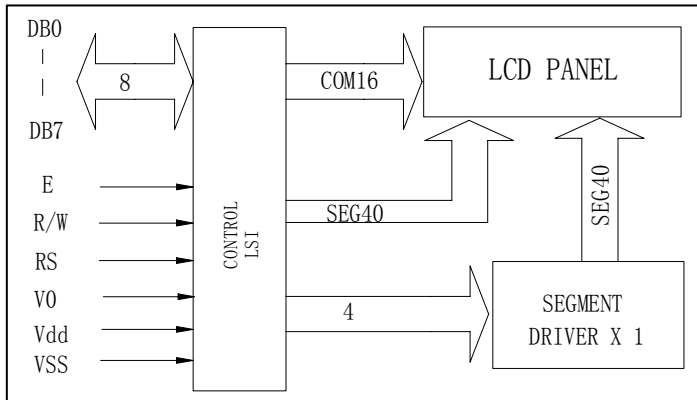
ELECTRICAL CHARACTERISTICS min max

Ta=25°C, Vdd=5.0V±0.5V
 Input 'high' voltage(ViH) ----- 3.5Vmin.
 Input 'low' voltage(ViL) ----- 0.55Vmax.
 Output 'high' voltage(VoH)(-IoH+0.1mA) -- 3.75Vmin.
 Output 'low' voltage(VoL)(IoL=1.2mA) ----- 1.0Vmax.
 Power supply current(Idd)(Vdd=5.0V) --- 2.5mAmax.
 Drive method ----- 1/16Duty, 1/5bias.

Pin No	Symbol	Lever	Function
1	Vss	—	VSS
2	Vdd	—	VDD+5V
3	Vo	—	Contrast adjustment
4	RS	H/L	H/L Register select signal
5	R/W	H/L	H/L Read/Write signal
6	E	H.H-L	Enable signal
7	DB0	H/L	Data bus line *
8	DB1	H/L	
9	DB2	H/L	
10	DB3	H/L	
11	DB4	H/L	
12	DB5	H/L	
13	DB6	H/L	
14	DB7	H/L	
15	LEDA		+5V for BKL
16	LEDK		Power supply for BKL(0V)

* In case of 4 bits instruction, data is transferred by twice using only 4 buses of D4-D7, and D0-D3 are not used, first operation is higher order 4 bits and second is lower 4 bits of 8 bits, but in case of 8 bits instruction, data is transferred by data by data bus of D0-D7.

BLOCK DIAGRAM



POWER SUPPLY

